

FIG. 1

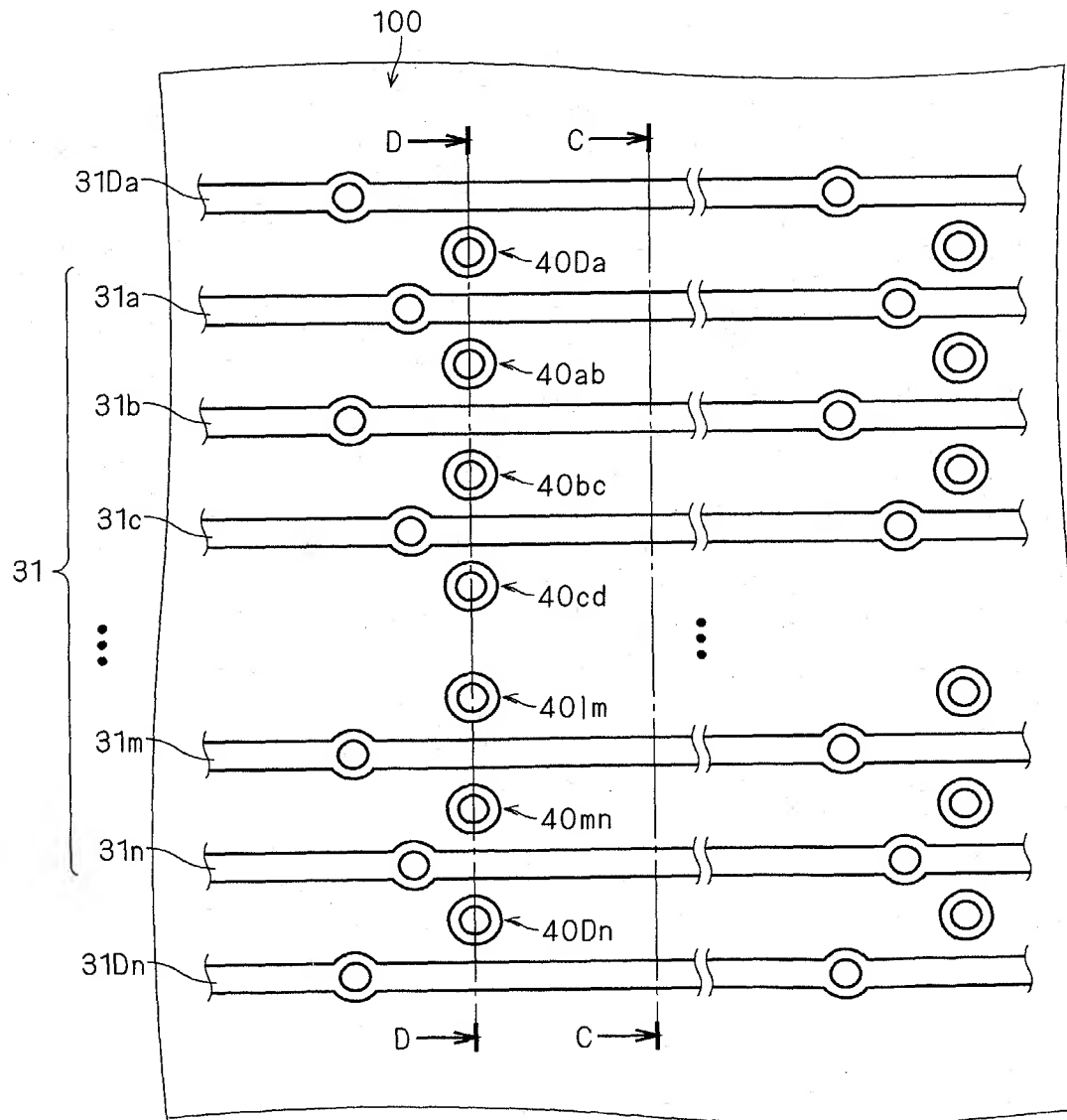


FIG. 2

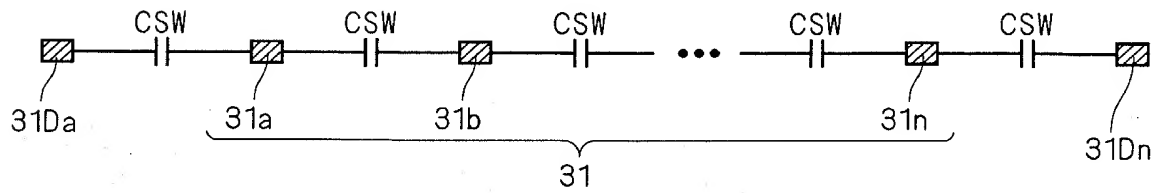


FIG. 3

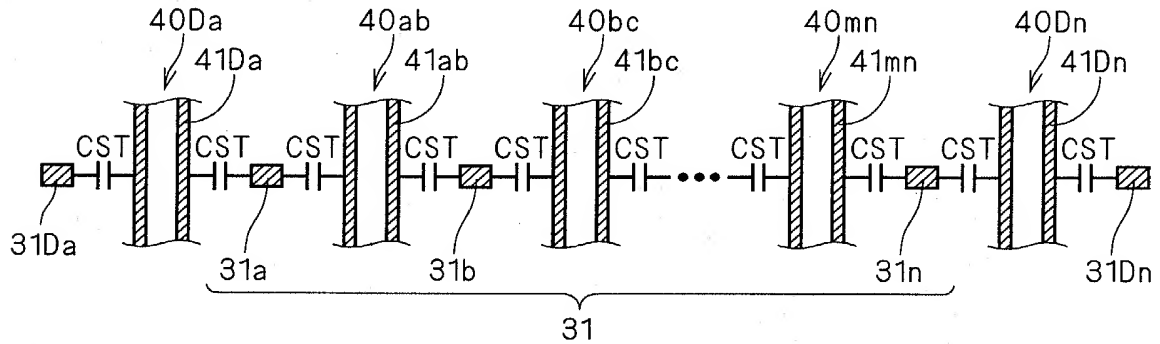


FIG. 4

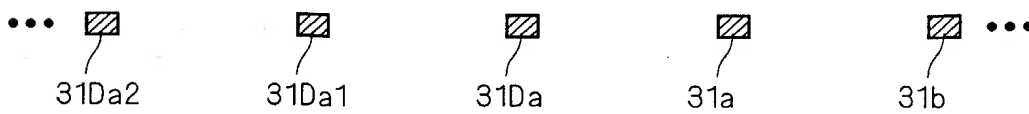


FIG. 5

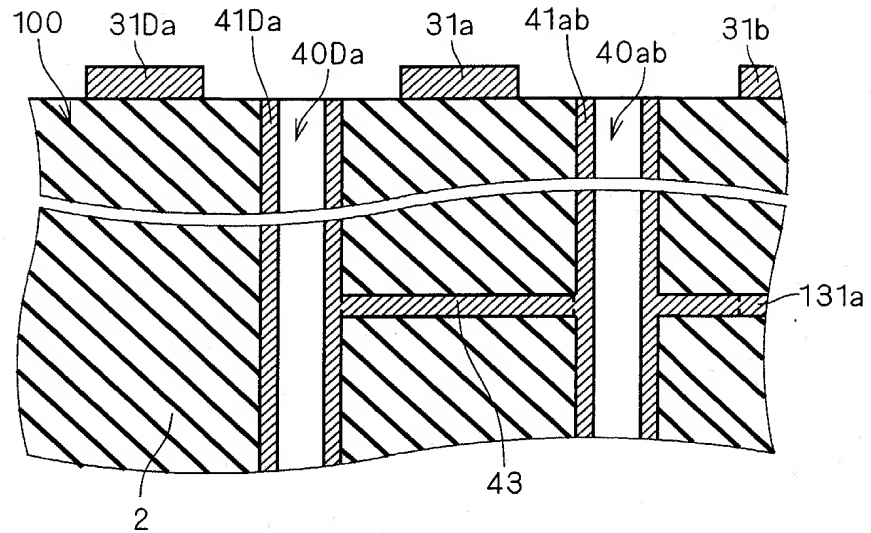


FIG. 6

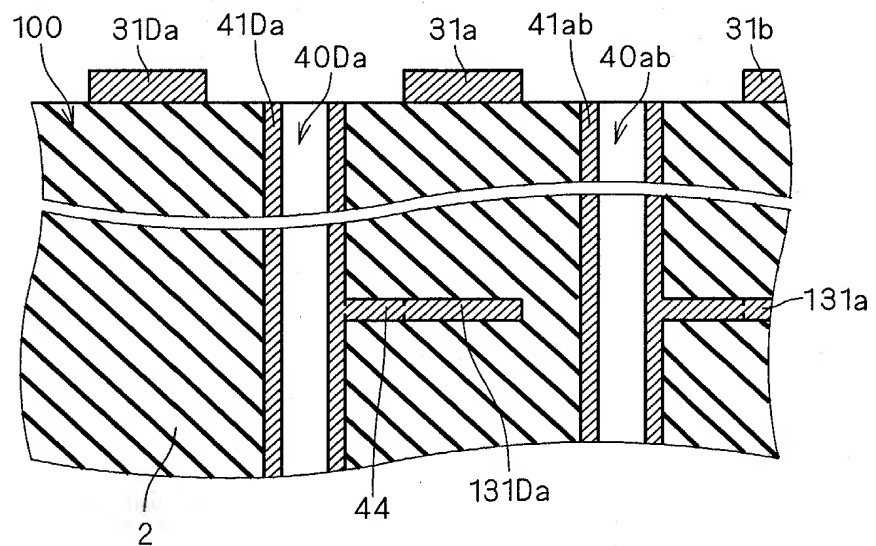


FIG. 7

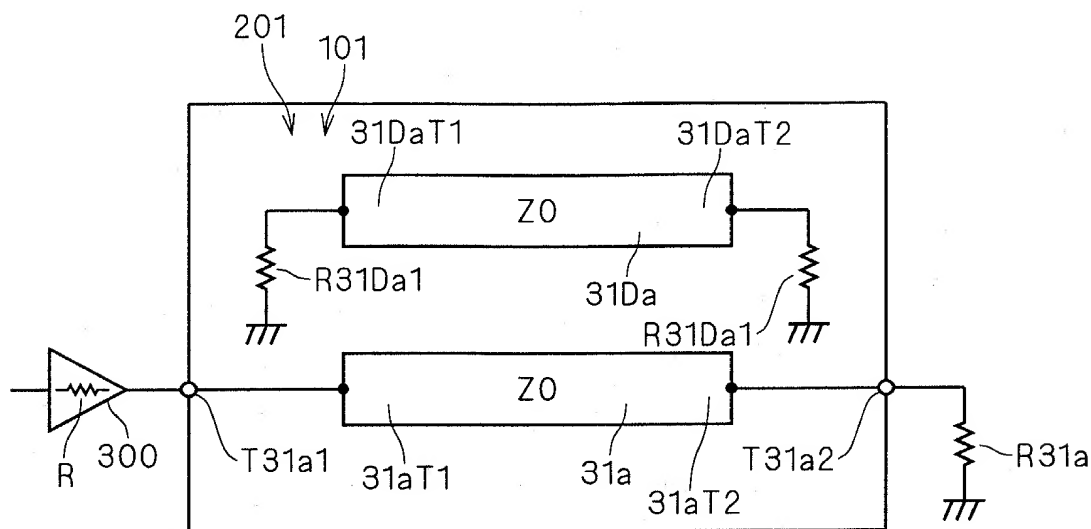


FIG. 8

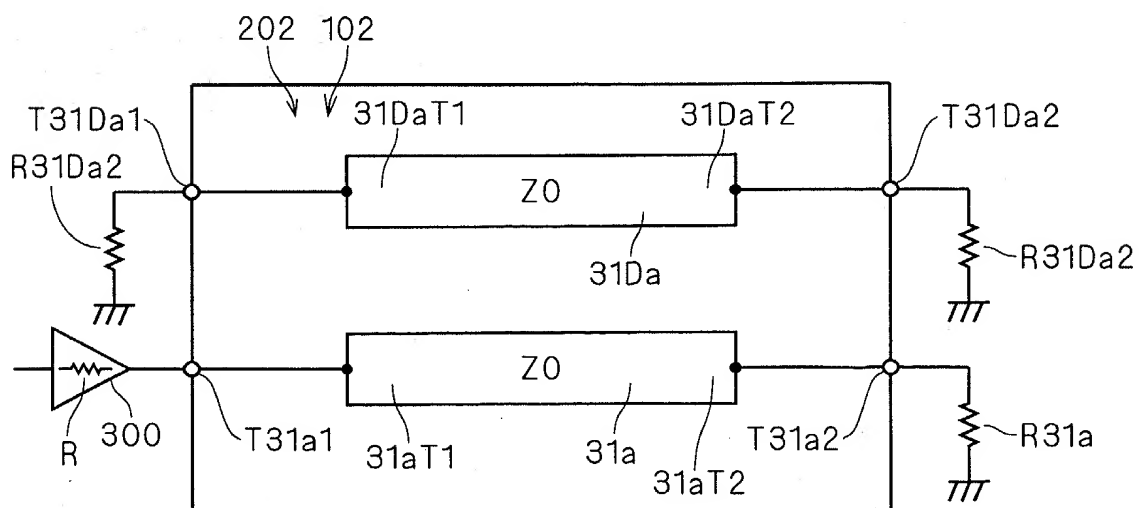


FIG. 9

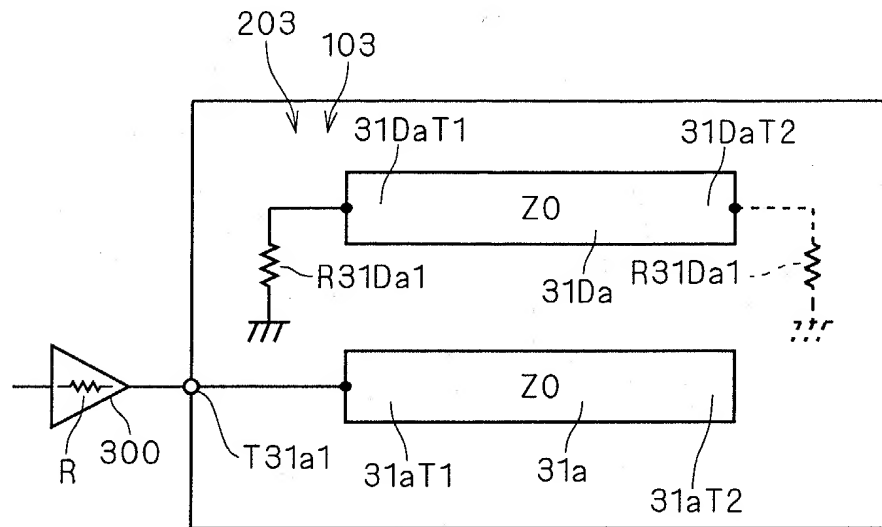


FIG. 10

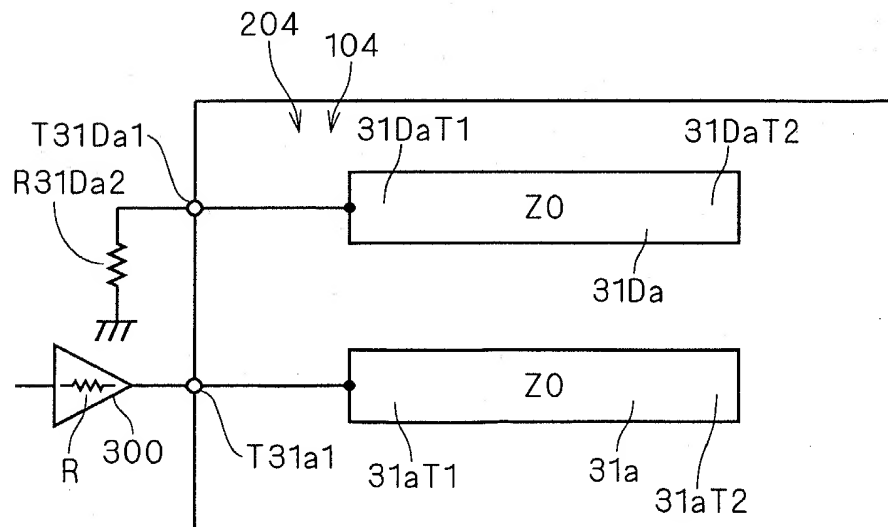


FIG. 11

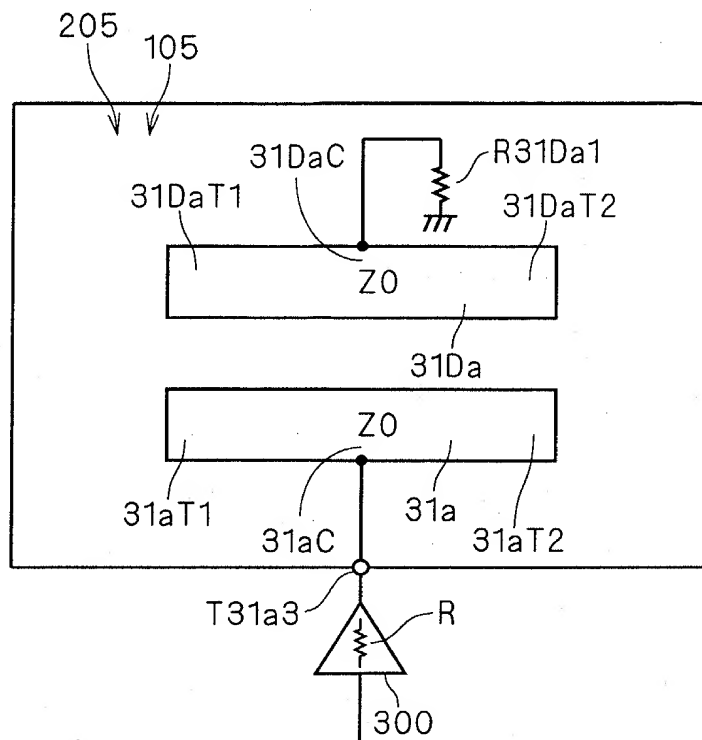
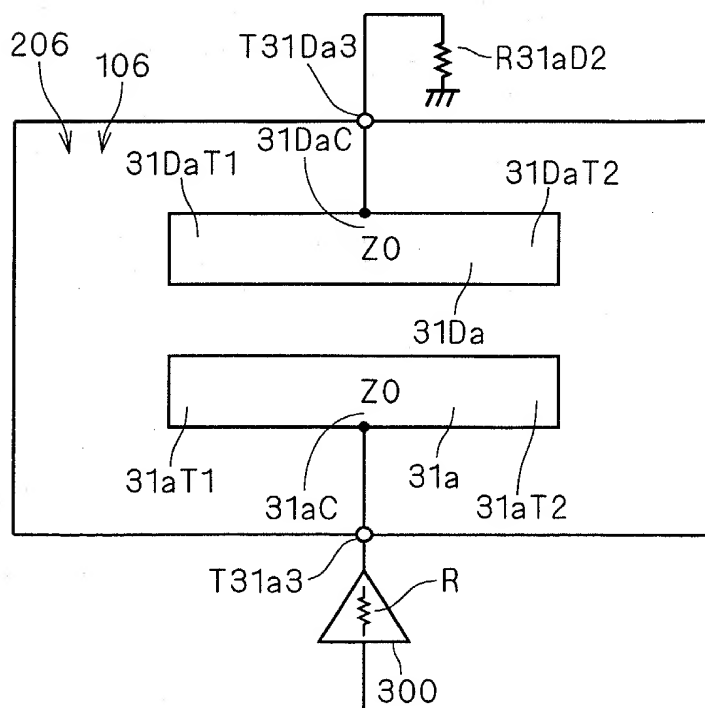


FIG. 12



[illegible]

The circuit diagram shows a differential amplifier circuit 208. It consists of two input nodes 108, two differential input transistors 31Da and 31a, and two differential output transistors 31Da2 and 31a2. The circuit is biased by a current source 300 and a resistor R. The output nodes are connected to ground through resistors R31Da2 and R31a2.

FIG. 15

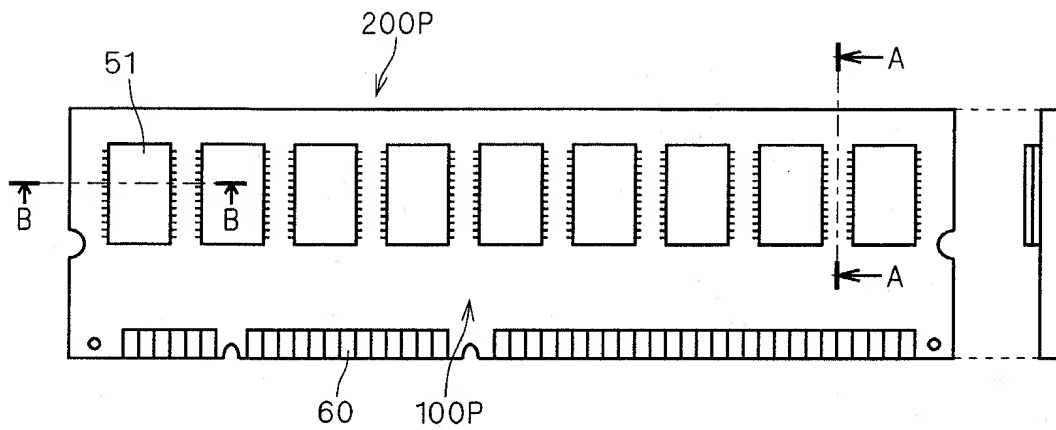


FIG. 16

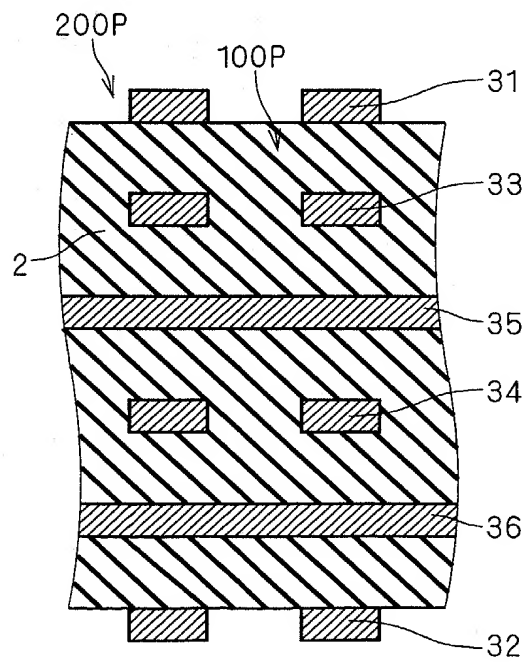




FIG. 17

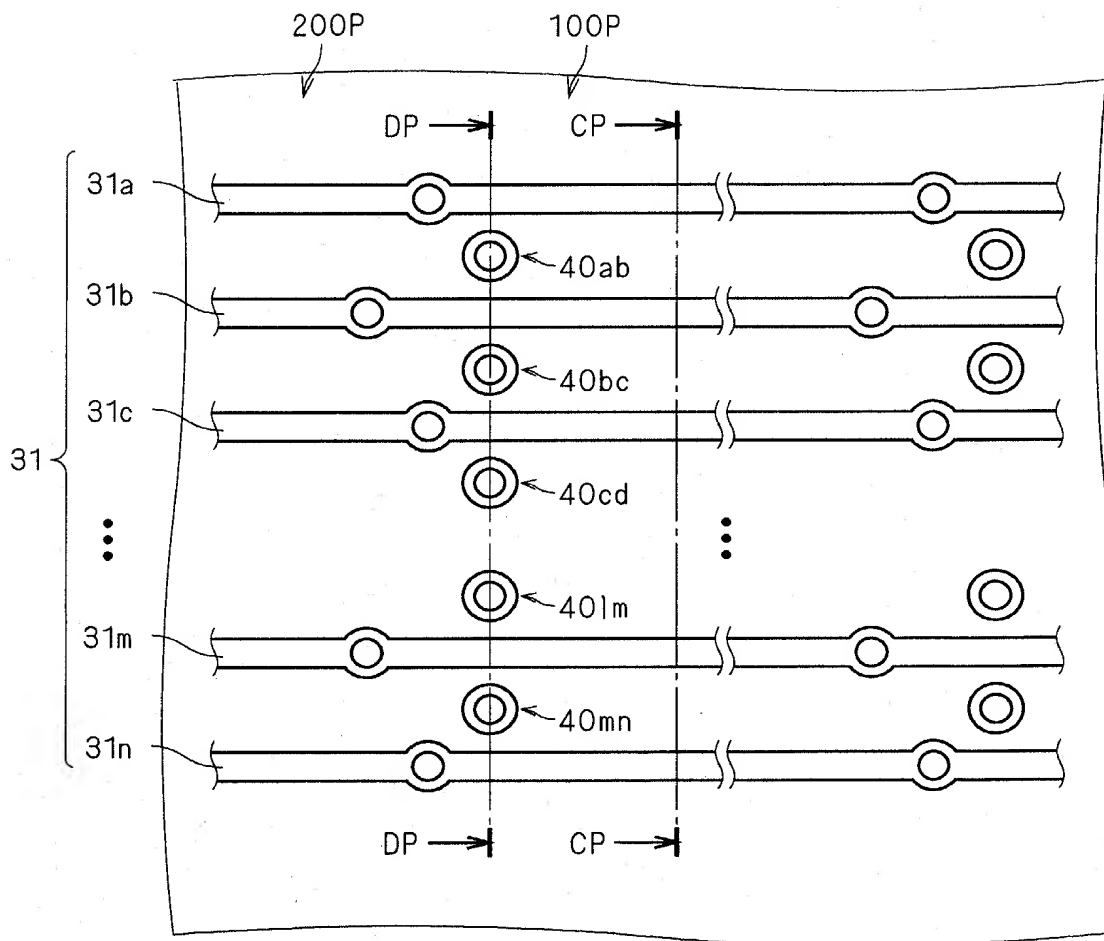


FIG. 18

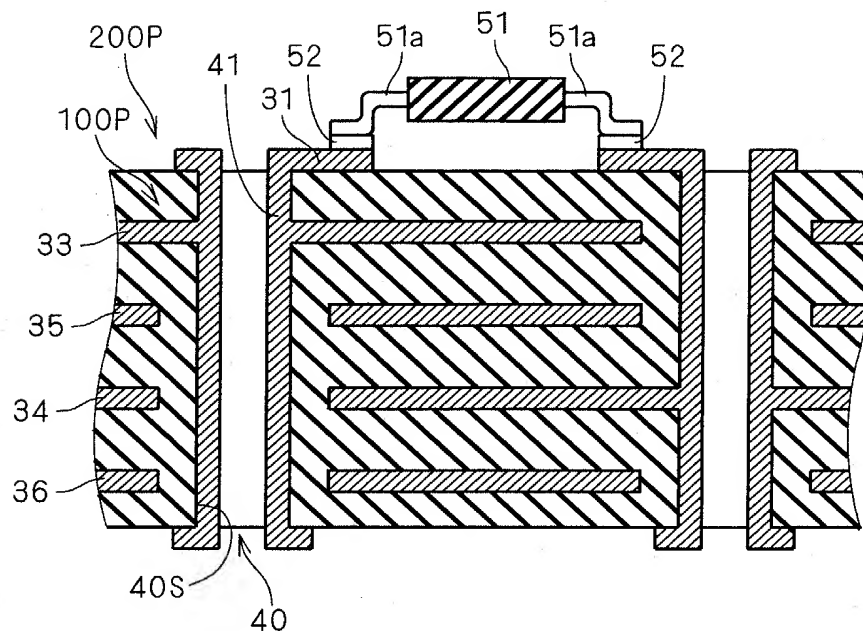


FIG. 19

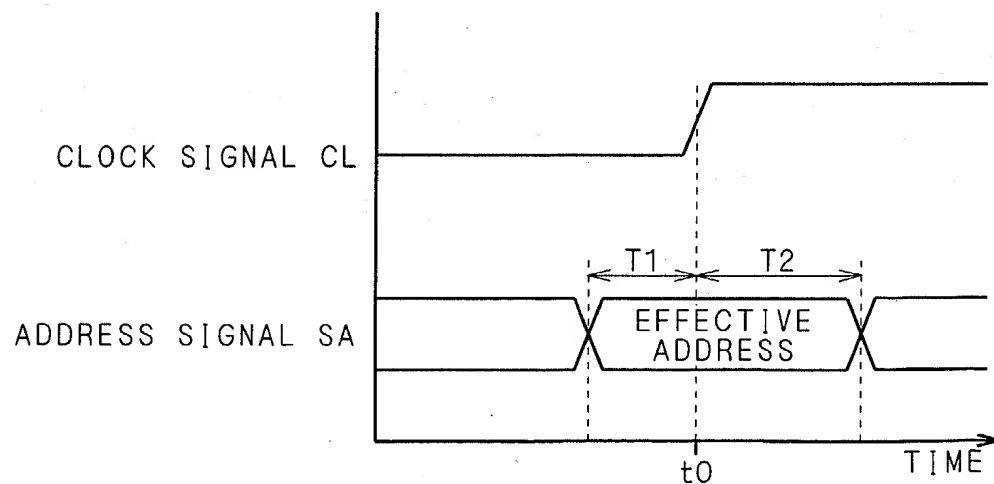


FIG. 20

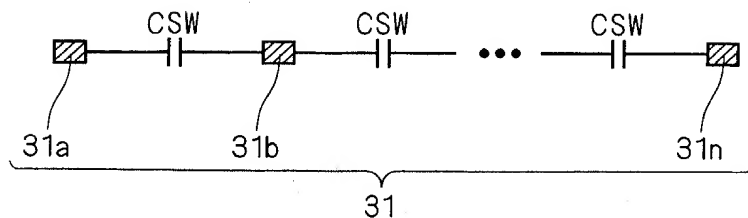


FIG. 21

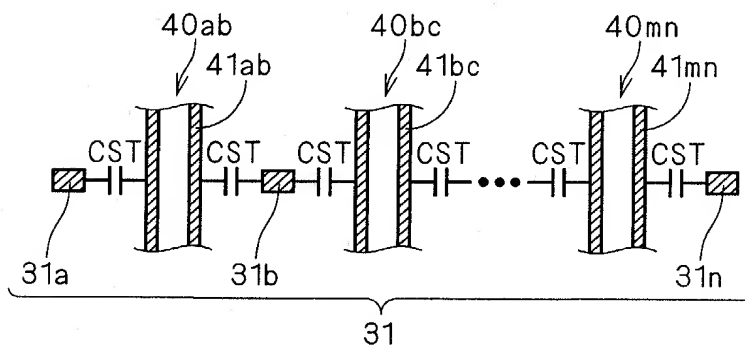


FIG. 22

